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U.S. PATENT DOCUMENTS□

EXAMIER INITIAL		DOCUMENT NO.	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPLICABLE

FOREIGN PATENT DOCUMENTS□

OTHER PRIOR ART (Including Author, Title, Date, Pertinent Pages, Etc.)□

JW		Kuang-Chien Chen, Jason Cong, Yuzheng Ding, Andrew Kahng, Peter Trajmar; *DAG-Map: Graph Based FPGA Technology Mapping For Delay Optimization;* IEEE Design and test of computers, pp 7-20, sept.1992.
JW		Jason Cong and Yuzheng Ding; *An Optimal Technology Mapping Algorithm for Delay Optimization in Lookup -Table Based FPGA Designs;* IEEE Trans. On Computer Aided Design of Integrated Circuits and Systems CAD, Vol.13, pp 1-12 Jan 1994.
JW		Jason Cong and Yuzheng Ding; *On Area/Depth Trade-off in LUT-Based FPGA Technology Mapping;* 30th ACM/IEEE design Automation Conference (DAC), pp. 213-218, 1993.
JW		Jason Cong and Yuzheng Ding; *Beyond the Combinatorial Limit in Depth Minimization for LUT-Based FPGA Designs;* IEEE/ACM International Conference on Computer Aided Design (ICCAD), pp. 110-114, Nov. 1993.

	10/12/05

EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw Line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.